

CASE 14342

CLAIM AMENDMENTS

1-8. (Canceled)

9. (Currently Amended) A dual stage current limiting surge protector system for protecting telecommunications equipment from power and transient surges, comprising:

input tip and ring terminal pins;

output tip and ring terminal pins;

first voltage suppressor means having first and second ends operatively coupled between said input tip and ring terminal pins;

the first and second ends of said first voltage suppressor means being also operatively coupled between said output tip and ring terminal pins;

first and second fuse elements interconnected between said input tip and ring terminal pins and the respective first and second ends of said first

-2-

CASE 14342

voltage suppressor means;

equipment to be protected being coupled to
said output tip and ring terminals;

third and fourth current fuse elements
interconnected between said output tip and ring
terminal pins and the respective first and second
ends of said first voltage suppressor means;

each of said third and fourth fuse elements
having a lower rated current value than each of
said first and second fuse elements;

said first and second fuse elements having a
relatively high rated current value of about 350
ma in order to allow passing a UL standard test;

said third and fourth fuse elements having a
relatively low rated current value of about 175 ma
in order to protect said equipment coupled to said
output tip and ring terminal pins from being
damaged;

CASE 14342

second voltage suppressor means having first and second ends operatively coupled between said output tip and ring terminal pins; and

said second voltage suppressor means having a predetermined breakdown voltage that is less than the breakdown voltage of said first voltage suppressor means.

10. (Canceled)

11. (Canceled)

12. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein said voltage suppressor means is comprised of a silicon avalanche suppressor.

13. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein said voltage suppressor means is comprised of a sidactor.

14. (Original) A dual stage current limiting surge

CASE 14342
protector system as claimed in Claim 9, wherein said voltage
suppressor means is comprised of a gas discharge tube.

15. (Canceled)

16. (Currently Amended) A dual stage current limiting
surge protector system for protecting telecommunications
equipment from power and transient surges, comprising:

input tip and ring terminal pins;

output tip and ring terminal pins;

first voltage suppressor means having first
and second ends operatively coupled between said
input tip and ring terminal pins;

the first and second ends of said first
voltage suppressor means being also operatively
coupled between said output tip and ring terminal
pins;

first and second positive thermal coefficient
resistors interconnected between said input tip

CASE 14342

and ring terminal pins and the respective first
and second ends of said first voltage suppressor
means;

equipment to be protected being coupled to
said output tip and ring terminals;

third and fourth positive thermal coefficient
resistors interconnected between said output tip
and ring terminal pins and the respective first
and second ends of said first voltage suppressor
means;

each of said third and fourth positive
thermal coefficient resistors having a lower rated
current value than each of said first and second
positive thermal coefficient resistors;

said first and second positive thermal
coefficient resistors having a relatively high
rated current value of about 160 ma in order to
allow passing a UL standard test;

CASE 14342

said third and fourth positive thermal coefficient resistors having a relatively low rated current value of about 80 ma in order to protect said equipment coupled to said output tip and ring terminal pins from being damaged;

second voltage suppressor means having first and second ends operatively coupled between said output tip and ring terminal pins; and

said second voltage suppressor means having a predetermined breakdown voltage that is less than the breakdown voltage of said first voltage suppressor means.

17. (Canceled)

18. (Canceled)

19. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a silicon avalanche

CASE 14342

suppressor.

20. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a sidactor.

21. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a gas discharge tube.

22. (Canceled)